

AMENDMENTS

LISTING OF CLAIMS:

1. **(Currently Amended)** An internal voltage generator of a semiconductor device, comprising:

 a tuning unit for receiving a test mode signal, an external signal and a signal stored in an internal setup device, and outputting a control signal;

 a characteristic controller for receiving the control signal, and outputting a characteristic controlling signal;

 an internal voltage generating unit for receiving a reference input signal and the characteristic controlling signal, and controlling a characteristic of an internal voltage;

 and

 a multiplexer for selectively outputting output signals from the internal voltage generating unit and from a data buffer to a data pad in response to the test mode signal.

 wherein when the test mode signal is activated the level of the control signal is determined by the external signal, and when the test mode signal is inactivated the level of the control signal is determined by the signal stored in the internal setup device.
2. **(Original)** The internal voltage generator according to claim 1, wherein the internal setup device is a fuse.
3. **(Currently Amended)** The internal voltage generator according to claim 1, wherein the internal voltage generator further comprises a demultiplexer for receiving a signal outputted from an address pad ~~in response to the test mode signal,~~ and outputting the signal into the tuning unit or an address decoder in response to the test mode signal.

4. (Canceled)

5. **(Original)** An internal voltage generator of a semiconductor device, comprising:

a first test mode block for receiving a test mode signal and a first external signal, and outputting a first characteristic control signal;

a second test mode block for receiving the test mode signal and a second external signal, and outputting a second characteristic control signal;

an internal voltage generating unit for receiving a reference input signal, the first characteristic control signal and the second characteristic control signal, and outputting an internal voltage; and

a data output unit for receiving the test mode signal, and outputting the internal voltage externally.

6. **(Original)** The internal voltage generator according to claim 5, wherein the first test mode block comprises:

a first tuning unit for receiving the test mode signal, the first external signal and a signal set in a first setup device, and outputting a predetermined control signal; and

a first characteristic controller for receiving the predetermined control signal, and outputting the first characteristic control signal.

7. **(Original)** The internal voltage generator according to claim 6, wherein the first setup device is a fuse.

8. **(Original)** The internal voltage generator according to claim 6, wherein the second test mode block comprises:

a second tuning unit for receiving the test mode signal, the second external signal and a signal set in a second setup device, and outputting a predetermined control signal; and

a second characteristic controller for receiving the predetermined control signal, and outputting the second characteristic control signal.

9. **(Original)** The internal voltage generator according to claim 8, wherein the second setup device is a fuse.

10. **(Original)** The internal voltage generator according to claim 5, wherein the internal voltage generating unit comprises:

a first amplifier for receiving the reference input signal;

a second amplifier for receiving an output signal from the first amplifier;

a first characteristic controller, connected between input/output terminals of the second amplifier, for receiving the first characteristic control signal;

a second characteristic controller, connected between the output terminal of the second amplifier and an output terminal of the internal voltage generating unit, for receiving the second characteristic control signal; and

a capacitor connected between the output terminal of the internal voltage generating unit and ground.

11. **(Original)** The internal voltage generator according to claim 10, wherein the first characteristic controller comprises a plurality of RC models connected in parallel between the input/output terminals wherein each of the plurality of RC models comprises a resistor and a capacitor connected in series.

12. **(Original)** The internal voltage generator according to claim 10, wherein the second characteristic controller comprises a plurality of resistors whose equivalent resistance is controlled by the second characteristic control signal.